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1. Power distribution techniques for VLSI circuits
Song, W.S.; Glasser, L.A.;
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2. Unconstrained via minimization for topological multilayer routing
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4. Design and fabrication of a high aspect ratio cable for a high gradient quadrupole magnet
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7. **Optimal n-tier multilevel Interconnect architectures for gigascale integration (GSI)**
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8. **Development of (Nb,Ta)₃Sn multifilamentary superconductors using Osprey bronze with high Abacherli, V.; Seeber, B.; Walker, E.; Flukiger, R.; Thiele, W.; Perenboom, J.A.A.J.;
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Volume 11, Issue 1, Part 3, March 2001 Page(s):3667 - 3670
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9. **Optimization of endoluminal loop radiofrequency coils for gastrointestinal wall MR Imaging**
Armenian, M.; Beuf, O.; Pilleul, F.; Saint-Jalmes, H.;
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10. **Interconnect and thermal-aware floorplanning for 3D microprocessors**
Hung, W.-L.; Link, G.M.; Yuan Xie; Vijaykrishnan, N.; Irwin, M.J.;
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McGuire, T.; Potter, R.;
[Magnetics, IEEE Transactions on](#)
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2. Device scaling limits of Si MOSFETs and their application dependencies
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1. Resonant tunneling through double barriers, perpendicular quantum transport phenomena in their device applications

Capasso, F.; Mohammed, K.; Cho, A.;

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Jin Soo Kim; Jin Hong Lee; Sung Ui Hong; Ho-Sang Kwack; Byung Seok Choi; Dae Kon Oh; [Photonics Technology Letters, IEEE](#)

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[AbstractPlus](#) | References | Full Text: [PDF\(912 KB\)](#) IEEE JNL[Rights and Permissions](#)**10. The In-line cryotron**

Brennemann, A.E.;

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Volume 51, Issue 3, March 1963 Page(s):442 - 451

[AbstractPlus](#) | Full Text: [PDF\(933 KB\)](#) IEEE JNL[Rights and Permissions](#)**11. A new circuit configuration for a single-transistor cell using Al-gate technology with reduced noise**

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[AbstractPlus](#) | Full Text: [PDF\(736 KB\)](#) IEEE JNL[Rights and Permissions](#)**12. The spatial distribution of flux waveform across the limbs in a three-phase transformer core**

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[AbstractPlus](#) | Full Text: [PDF\(472 KB\)](#) IEEE JNL[Rights and Permissions](#)**13. Bibliography of thin magnetic films (1963-1967)**

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15. TRIM: testability range by ignoring the memory

Carter, L.; Huisman, L.M.; Williams, T.W.;

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17. Poisson modeling of ultraconfined AlGaAs-GaAs semiconductor devices with selective dop

Bigelow, J.M.; Leburton, J.P.; Klejwa, M.; Adesida, I.;

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IEEE CNF IEEE Conference Proceeding



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